A False History of True Concurrency — from Petri to tools

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## 



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Lambda calculus (Church 35)

Turing machines (Turing 36)

Finite automata (Kleene 56, Moore 56, Mealy 56, Scott and Rabin 59)

Pushdown automata (Oettinger 61, Chomsky 62)

## **Semantics: executions**

**Configurations**: set of informations completely describing the state of the machine at a certain moment in time

Transitions: moves between configurations

Lambda calculus	$(\lambda {m x}.{m x}{m x})(\lambda {m y}.{m y})$	$\longrightarrow$	$(\lambda y.y)(\lambda z.z)$
Turing machine	$0010 {m q}_1 011$	$\longrightarrow$	$001 q_2 01011$
Finite automaton	$oldsymbol{q}_1$	$\xrightarrow{a}$	${oldsymbol q}_2$
Pushdown automaton	$(oldsymbol{q}_1,oldsymbol{X}oldsymbol{Y}oldsymbol{Z})$	$\xrightarrow{a}$	$(q_2, XYXYYZ)$

Executions: alternating sequences of configurations and transitions



Abstract machines	are implemented as can simulate	physical machines
	can simulate	physical machine

#### SIMULA project (Nygaard and Dahl), started in 1962

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A plane (physical machine)

can be simulated by a flight simulator (abstract machine)

which can be implemented in a video console (physical machine)

which can be simulated by a hardware simulator (abstract machine)

which is implemented in a PC (physical machine) ....

## **Petri's question**

In 1962, C.A. Petri (1926–2010) points out a discrepancy between how Theoretical Physics and Theoretical Computer Science describe systems (at that time):

Theoretical Physics describes systems as a collection of interacting particles (subsystems), without a notion of global clock or simultaneity

Theoretical Computer Science describes systems as sequential virtual machines going through a temporally ordered sequence of global states

Petri's question:

Which kind of abstract machine should be used to describe the physical implementation of a Turing machine?

















## **Execution semantics**

An execution semantics

State: marking (distribution of tokens)

Transitions:  $M \xrightarrow{a} M'$ 

**Executions**:  $M_0 \xrightarrow{a_0} M_1 \xrightarrow{a_1} M_2 \dots$ 













 $\begin{array}{c} \mathbf{s} \\ \mathbf{r} \\ \mathbf{q} \end{array} \begin{bmatrix} 0 \\ 0 \\ 0 \end{bmatrix} \xrightarrow{\mathbf{c}} \begin{bmatrix} 0 \\ 1 \\ 0 \end{bmatrix}$ 



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11





 $q_0$ 

 $r_1$ 





 $q_0$ 





 $q_0$ 





 $q_0$ 









































The argument for executions:

The global clock assumption is adequate for practical purposes, and leads to nice mathematics

The argument against executions:

The global clock assumption does not correspond to physical reality, and leads to awkward representations of simple phenomena

### The standard example

A system composed of *n* independent components



has n! different executions, and 2 global states.

The system has only one concurrent execution of size O(n).

## 



### **Execution trees**



Nielsen, Plotkin, Winskel '81: What could be a "concurrent execution tree" ?







 $r_1$ 

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 $r_1$ 













# The unfolding



Motivation of Nielsen, Plotkin and Winskel purely semantic

Denotational semantics of the concurrent behaviour of a Petri net

Definition of a domain analogous to Scott's domain for computable functions

Extension of Scott's thesis 'he computable functions are the continuous functions' to concurrency.

## 



## The state-explosion problem

A system composed of *n* independent components



has  $2^n$  reachable states.

Its unfolding is the system itself, and has size O(n)

McMillan: can the unfolding help palliate the state-explosion problem?

## The executability problem

Executability: does some execution of the net contain a given action ?

Fact: model-checking safety properties  $\rightarrow$  executability problem.

- Goal: design search algorithms that instead of exploring the execution tree explore the unfolding.
- Hope: the algorithms need to explore a small prefix of the unfolding, instead of a large prefix of the execution tree.

# Search algorithm

A search algorithm is determined by:

A search strategy.
Fixes which event to add next.
In this talk we assume deterministic strategies.

#### • A termination condition.

Fixes which leaves of the current prefix are terminals, i.e., nodes whose successors need not be explored.

## Searching the execution tree

Search algorithm for executability of an action g

Search strategy: any.

Termination condition: An event is a terminal if

- it is labeled by **g** or,
- it leads to the same state as another already explored event.

## Example


















# **Searching the unfolding**

We want something like this:

Termination condition: An event is a terminal if

- it is labeled by g or,
- "it leads to the same global state as another already executed event."

But an event of an unfolding does not usually lead to a unique global state



But an event of an unfolding does not necessarily lead to one global state



Solution (McMillan '92): attach to an event the global state reached by "executing its past".

Call it the McMillan state























## First attempt at a search algorithm

Search procedure to decide executability of g

Search strategy: any.

Termination condition: An event is a terminal if

• it is labeled by g or,

• its McMillan state is the same as the McMillan state of another already explored event.



#### Is the search algorithm correct ?

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# No !!





## **McMillan's solution**

Stronger termination condition: An event *e* is a terminal if

- it is labeled by **g** or,
- its McMillan state is the same as the McMillan state of another, already executed, event e'

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Stronger termination condition: An event e is a terminal if

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Downside: stronger condition  $\rightarrow$  fewer terminals  $\rightarrow$  larger prefix

Worst case: prefix exponentially larger than number of reachable global states.





## 



A better solution by E., Römer, and Vogler.

General idea: Keep the termination condition, restrict the strategies.

The past of an event can be seen as a Mazurkiewicz trace: the set of all possible ways of executing the past.



Selecting the next event amounts to selecting its Mazurkiewicz trace.

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Strategy  $\rightarrow$  total order  $\prec$  on Mazurkiewicz traces.

A strategy  $\prec$  on Mazurkiewicz traces is adequate if  $t \prec t'$  implies  $t t'' \prec t' t''$  for all t''.

## Are there adequate strategies?

Fact 1: every adequate strategy on executions can be "lifted" to an adequate strategy on Mazurkiewicz traces.

Fact 2: the following strategy on executions is adequate:  $W_1 \prec W_2$  iff

- $|W_1| < |W_2|$ , or;
- $|W_1| = |W_2|$  and  $W_1$  is lexicographically smaller than  $W_2$ .

Many adequate strategies have been found: E., Römer, Vogler '96, E., Römer '99, Niebert, Qu '06.
Prefixes of the unfolding containing all reachable states.





Prefixes of the unfolding containing all reachable states.





Compact representations of the state space.

System	Structured Petri net			Prefix		BDD size
(scale)	Places	Trans.	States	Places	Trans.	(Petrify)
Cycl(12)	95	71	74264	232	104	
PhilD(7)	63	63	109965	86310	4314	
SIRing(10)	100	100	$8.1 \times 10^{12}$	119450	86180	
Elevator(4)	736	1939	43440	32354	16935	
ProdCell	231	202	$>$ 3.1 $\times 10^{6}$	2164	1035	40188
ConcPush	150	140	$2.8 \times 10^{7}$	1671	780	210249
DMut(64)	257	256	$1.8 \times 10^{62}$	385	256	45460
RW(10)	86	66	$1.6 \times 10^{6}$	29132	15974	7576

100 random tables with right-handed, left-handed, and ambidextrous philosophers

Nr.	of	BDD size					
phi	Ι.	Average	Min.	Max.	St.Dev.	Aver./St.Dev.	
4		178	94	355	52	0.30	
6		583	248	1716	305	0.52	
8		1553	390	8678	1437	0.92	
10		3140	510	27516	4637	1.48	
12		4855	632	47039	8538	1.76	
14	1	33742	797	429903	85798	2.54	

BDD for the set of reachable states (Petrify)

100 random tables with right-handed, left-handed, and ambidextrous philosophers

Nodes of the complete prefix (PEP)

Nr. of	Prefix size				
phil.	Average	Min.	Max.	St.Dev.	Aver./St.Dev.
4	46	40	60	5.13	0.10
6	70	60	85	5.99	0.09
8	95	80	110	6.92	0.07
10	117	100	135	7.78	0.07
12	141	120	160	7.40	0.05
14	161	140	185	9.25	0.06

Prefixes of the unfolding containing all reachable states.

Compact representations of the state space.

But the net itself is already an (even more) compact representation!

Trade-off between compactness and query complexity.

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Reachability of a global state:

<u>Petri net</u>	Complete prefix	Transition system
PSPACE-complete		Linear

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#### 





# Querying a prefix with SAT

First studied by Keijo Heljanko in his thesis and several papers.



### **Checking deadlock-freedom with BDDs**

100 random tables with right-, left-handed, and ambidextrous philosophers

SMV on a SUN Ultra 60, 2 processors, 640 MB (old experiment)

Nr of	Time in seconds					
phil.	Average	Min.	Max.	St.Dev.	Aver./St.Dev.	
4	0.08	0.05	0.13	0.02	0.29	
6	0.36	0.20	1.18	0.16	0.46	
8	4.14	1.25	14.60	2.45	0.59	
10	56.60	15.80	388.00	46.90	0.83	
12	1595.00	228.00	10616.00	1615.00	1.01	

## Checking deadlock-freedom with unfoldings

100 random tables with right-, left-handed, and ambidextrous philosophers

PEP + stable models on a SUN Ultra 60, 2 processors, 640 MB (old experiment)

Nr. of	Time in seconds					
phil.	Average	Min	Max	St. Dev	Aver./St. Dev	
8	0.01	0.04	0.03	0.007	0.24	
10	0.01	0.06	0.03	0.009	0.27	
12	0.02	0.07	0.04	0.012	0.28	
14	0.02	0.05	0.04	0.007	0.20	
16	0.02	0.05	0.04	0.007	0.17	
18	0.03	0.05	0.04	0.007	0.17	

#### 2000-2010

# 2000-2010





### 2000-2010



### A quick summary

• Efficient construction of complete prefixes, adequate strategies. Chatain, E., Khomenko, Koutny, Niebert, Schwoon, Vogler . . .

• Full LTL model-checking. Couvreur, E., Heljanko, Grivet, Poitrenaud ...

• Parallel and distributed generation of the unfolding. Baldan, Haar, Heljanko, Khomenko, König, Koutny ...

• Extensions to other concurrency models. Baldan, Corradini, Haar, Khomenko, König, Langerak, Meyer, Schröter, Schwoon . . .

• Extensions to timed models. Bouyer, Cassez, Chatain, Haddad, Jard ...

### **Applications I: Asynchronous circuits**

Circuits specified as Signal Transition Graphs (interpreted Petri nets)

Transitions raise and lower boolean signals

Correctness: every global state completely determined by the values of the signals

Concurrent Asynchronous Systems Group, University of Newcastle: tool-chain for verification and fault-fixing of STGs based on unfoldings.

Khomenko, Schäfer, Vogler, Yakovlev, Wollowski ...

# **Applications II: Monitoring and diagnosis**

Distributed systems with sensors attached to some nodes

Alarms reported to global supervisor, who performs diagnosis

Problem: find cause of the alarms  $\rightarrow$  true-concurrency approach

IRISA group in Rennes, MEXICO project at ENS Cachan: diagnosis tools.

Benveniste, Chatain, Haar, Jard ...

# **Applications III: Graph Grammars**

Unfolding used to overapproximate the set of graphs generated by graph transformation systems

"Folding the unfolding" yields an unbounded Petri net, with each marking representing a graph.

Model-checking algorithms based on the overapproximation

AUGUR 1 and 2.

Baldan, Corradini, König, Kozioura ...

#### **Applications III: Graph Grammars**



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### Conclusions

From abstract connections between Physics and Computer Science to concrete algorithms and applications.

Turning point: verification through explicit construction of semantic objects.

True-concurrency useful in two ways:

- Compact representation of state spaces.
- Information about causality (diagnosis) and independence (probabilistic systems)